### **Vishay High Power Products**

# Schottky Rectifier, 5.5 A

### **FEATURES**

- Popular D-PAK outline
- · Small foot print, surface mountable
- · Low forward voltage drop
- High frequency operation
- · Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS directive 2002/95/EC
- · AEC-Q101 qualified

### DESCRIPTION

The 50WQ06FNPbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

| MAJOR RATINGS AND CHARACTERISTICS |                                |             |       |  |
|-----------------------------------|--------------------------------|-------------|-------|--|
| SYMBOL                            | CHARACTERISTICS                | VALUES      | UNITS |  |
| I <sub>F(AV)</sub>                | Rectangular waveform           | 5.5         | A     |  |
| V <sub>RRM</sub>                  |                                | 60          | V     |  |
| I <sub>FSM</sub>                  | t <sub>p</sub> = 5 μs sine     | 320         | A     |  |
| V <sub>F</sub>                    | 5 Apk, T <sub>J</sub> = 125 °C | 0.54        | V     |  |
| TJ                                | Range                          | - 40 to 150 | °C    |  |

| VOLTAGE RATINGS                      |                  |             |       |  |
|--------------------------------------|------------------|-------------|-------|--|
| PARAMETER                            | SYMBOL           | 50WQ06FNPbF | UNITS |  |
| Maximum DC reverse voltage           | V <sub>R</sub>   | 60          | V     |  |
| Maximum working peak reverse voltage | V <sub>RWM</sub> | 80          |       |  |

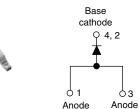
| ABSOLUTE MAXIMUM RATINGS                               |                        |   |  |       |    |
|--|------------------------|---|--|-------|----|
| PARAMETER  | SYMBOL TEST CONDITIONS |   | VALUES   | UNITS |    |
| Maximum average forward current<br>See fig. 5          | I <sub>F(AV)</sub>     | $I_{F(AV)}$ 50 % duty cycle at T <sub>C</sub> = 132 °C, rectangular waveform  |  | 5.5   |    |
| Maximum peak one cycle<br>non-repetitive surge current | I <sub>FSM</sub>       | 5 $\mu s$ sine or 3 $\mu s$ rect. pulse   | Following any rated<br>load condition and with<br>rated V <sub>RRM</sub> applied | 320   | A  |
| See fig. 7   |                        | 10 ms sine or 6 ms rect. pulse  |  | 105   |    |
| Non-repetitive avalanche energy                        | E <sub>AS</sub>        | $T_J = 25 \text{ °C}, I_{AS} = 1.2 \text{ A}, L = 10 \text{ mH}$  |  | 7     | mJ |
| Repetitive avalanche current                           | I <sub>AR</sub>        | Current decaying linearly to zero in 1 $\mu$ s<br>Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical |  | 0.8   | А  |



**PRODUCT SUMMARY** 

I<sub>F(AV)</sub>

 $V_R$ 



5.5 A

60 V





## 50WQ06FNPbF

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| ELECTRICAL SPECIFICATIONS                     |                                |  |                                       |        |       |
|---|--------------------------------|--|---------------------------------------|--------|-------|
| PARAMETER                                     | SYMBOL                         | TEST CONDITIONS  |                                       | VALUES | UNITS |
| Maximum forward voltage drop<br>See fig. 1    | V <sub>FM</sub> <sup>(1)</sup> | 5 A  | T.I = 25 °C                           | 0.57   | V     |
|   |                                | 10 A   | $I_{\rm J} = 25 {}^{\circ}{\rm C}$    | 0.74   |       |
|   |                                | 5 A  | T.I = 125 °C                          | 0.54   |       |
|   |                                | 10 A   |                                       | 0.68   |       |
| Maximum reverse leakage current<br>See fig. 2 | I <sub>RM</sub> <sup>(1)</sup> | T <sub>J</sub> = 25 °C   | V <sub>B</sub> = Rated V <sub>B</sub> | 3      | mA    |
|   |                                | T <sub>J</sub> = 125 °C  | V <sub>R</sub> = naleu V <sub>R</sub> | 35     |       |
| Threshold voltage                             | V <sub>F(TO)</sub>             | - T <sub>J</sub> = T <sub>J</sub> maximum -                    |                                       | 0.35   | V     |
| Forward slope resistance                      | r <sub>t</sub>                 |  |                                       | 25.5   | mΩ    |
| Typical junction capacitance                  | CT                             | $V_{R} = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C |                                       | 360    | pF    |
| Typical series inductance                     | L <sub>S</sub>                 | Measured lead to lead 5 mm from package body                   |                                       | 5.0    | nH    |
| Maximum voltage rate of change                | dV/dt                          | Rated V <sub>R</sub>   |                                       | 10 000 | V/µs  |

#### Note

 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

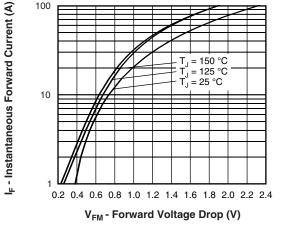
| THERMAL - MECHANICAL SPECIFICATIONS            |                                    |  |             |       |
|--|------------------------------------|--|-------------|-------|
| PARAMETER                                      | SYMBOL                             | TEST CONDITIONS                        | VALUES      | UNITS |
| Maximum junction and storage temperature range | $T_{J}$ <sup>(1)</sup> , $T_{Stg}$ |  | - 40 to 150 | °C    |
| Maximum thermal resistance, junction to case   | R <sub>thJC</sub>                  | DC operation<br>See fig. 4             | 3.0         | °C/W  |
| Approximate weight                             |                                    |  | 0.3         | g     |
| Approximate weight                             |                                    |  | 0.01        | oz.   |
| Marking device                                 |                                    | Case style D-PAK (similar to TO-252AA) | 50WC        | 06FN  |

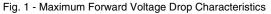
#### Note

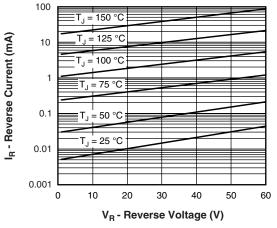
(1)  $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$  thermal runaway condition for a diode on its own heatsink

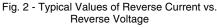


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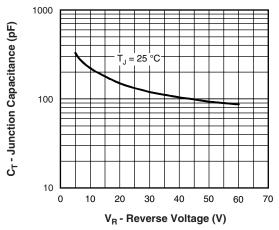


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

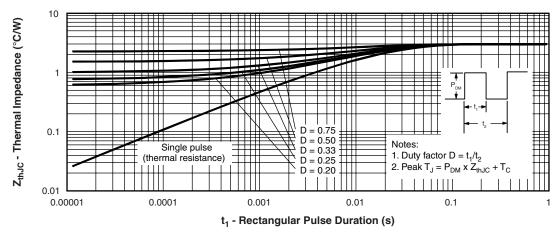
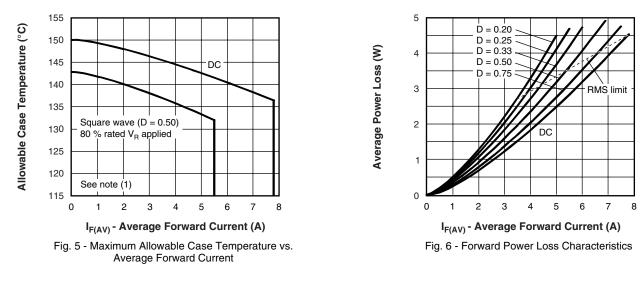
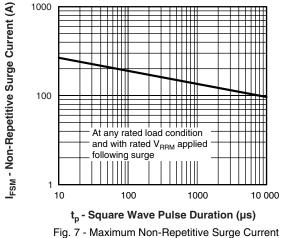


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics

## 50WQ06FNPbF

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#### Note

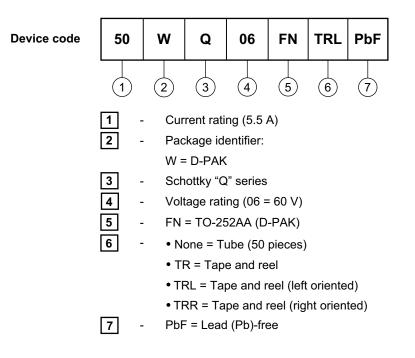
- <sup>(1)</sup> Formula used:  $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC};$   $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$  (see fig. 6);  $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 D); I_R at V_{R1} = 80 \% rated V_R$





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### ORDERING INFORMATION TABLE



| LINKS TO RELATED DOCUMENTS |                          |  |  |
|----------------------------|--------------------------|--|--|
| Dimensions                 | www.vishay.com/doc?95016 |  |  |
| Part marking information   | www.vishay.com/doc?95059 |  |  |
| Packaging information      | www.vishay.com/doc?95033 |  |  |



Vishay

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